

Dr. Rinaldo Poluzzi was born in Bologna in 1941 (Italy) where he obtained the degree (PHD) in Theoretical Physics. In 1970 reached the research Lab for Solid State Microelectronic of STMicroelectronic (STM) in Agrate Brianza (Milano) where he worked for 10 year as responsible for the design of First Integrated Components for industrial and Telecommunication applications. In 1982 joined the Strategic Marketing Unit of STM where he was entrusted for the Specs Definition for advanced digital Very Large Scale Integration (VLSI) for numeric transmission for Central Telephone Exchange and afterwards for Standardization and Advanced Digital Components of High Definition TV Set (HDTV). In 1992 he started the Systemic Research for the sw/hw implementation of dedicated components for Artificial Intelligence (Neuronal Nets and Fuzzy Logic ) collaborating with national and foreign universities within financed external national and European research programs. In 1996 appointed in the STM organization as Chief Scientist of AST (Advanced System Technology), an internal research group dedicated to development of future systems on silicon. In 2000 he attended in AST as consultant to the definition of systems based on hybrid bio nanoscale range technologies dedicated to emerging Lab-on-Chip for DNA analysis and micro sensors based Micro Electric Mechanical Systems (MEMS) for non-invasive medical analysis. He is author of several papers and registered patents.